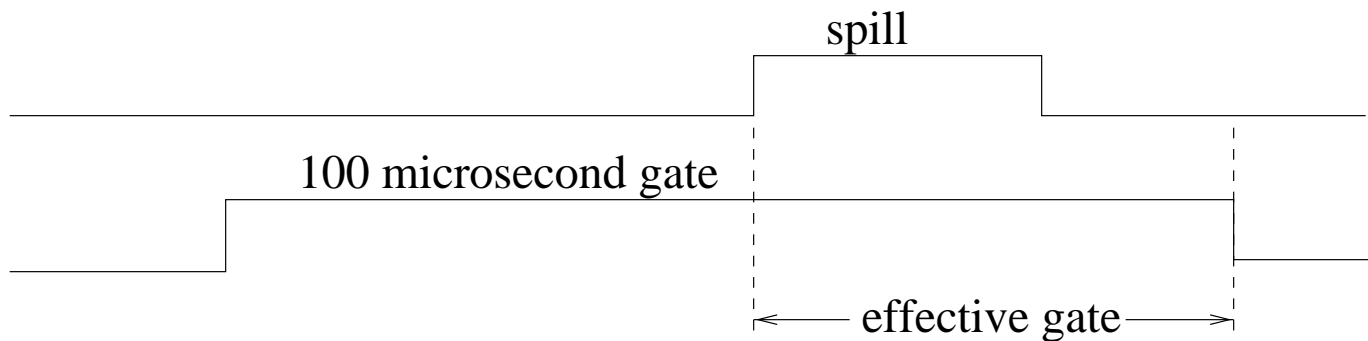


5. At the present time the shortest gate width for the SWIC electronics is 100 microseconds. (Perhaps this can be reduced to 10 microseconds or smaller by reprogramming the gate arrays and maybe also the VAX in the accelerator division.) Since the leading edge of the gate can be placed to a microsecond (probably better than 0.1 microsecond), we can shorten the effective gate width by starting it earlier than the spill:



At the beginning of the run we need some time to adjust a comfortable effective gate width. A 100 microsecond gate is probably too long, as it may then collect something like 10% of the ions as well as all of the electrons. We should make frequent scans of the effective gate width to make sure any effects we observe are not sensitive to this width.